Refine Search

Search Results -

Terms	Documents
((number or rate or total) near3 request) near10 (activity near3 bus)	4

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<u>L3</u>	request near10 (activity near3 bus)	261	<u>L3</u>
DB=U	JSPT; PLUR=YES; OP=OR		
<u>L2</u>	6073244.pn. or 5628019.pn.	2	<u>L2</u>
<u>L1</u>	6185692.pn.	1	<u>L1</u>

Freeform Search

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	6073244.pn. or 5628019.pn.	2	L2
	5185692.pn.	1	<u>L2</u> L1
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Refine Search

Search Results -

Terms	Documents
((variable adj1 speed) near5 bus) same (clock near3 frequency)	4

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Search History

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<u>L1</u> ((variable adj1 speed) near5 bus) same (clock near3 frequency)

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Freeform Search

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Refine Search

Search Results -

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(request near5 (rate or number)) same (activity near3 bus)	35

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<u>L2</u>	(variable adj1 speed) same bus same (clock near5 frequency)	9	<u>L2</u>
<u>L1</u>	((variable adj1 speed) near5 bus) same (clock near3 frequency)	4	<u>L1</u>

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- <u>L3</u> (var\$5 near3 speed) same bus same (clock near5 frequency)
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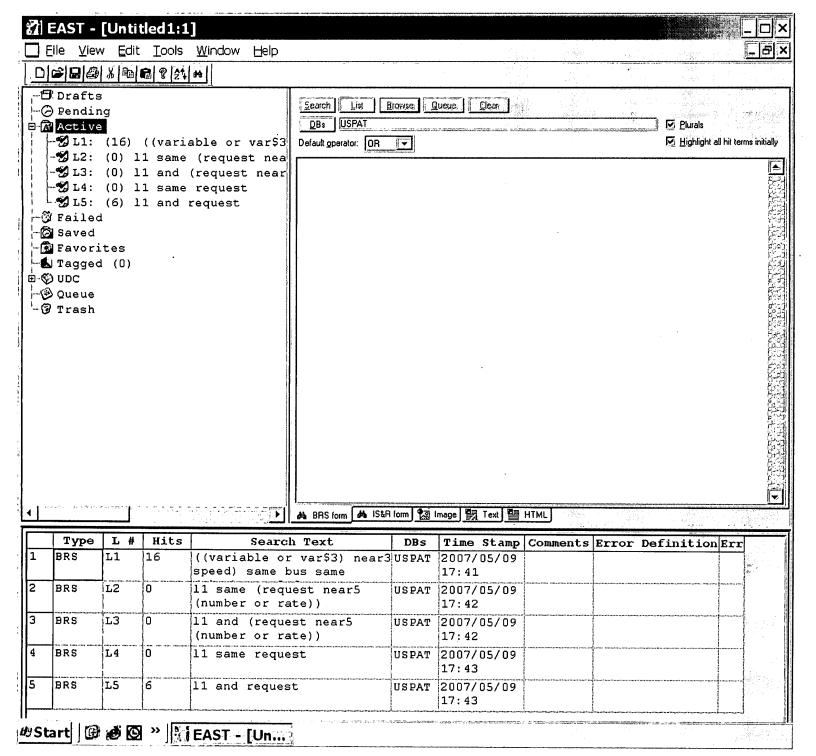
Refine Search Search Results -Terms Documents L5 and L6 US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database Database: **EPO Abstracts Database** JPO Abstracts Database Derwent World Patents Index **IBM Technical Disclosure Bulletins** L7 Search: Refine Search Recall Text = Interrupt Clear **Search History** DATE: Wednesday, May 09, 2007 **Purge Queries** Printable Copy Create Case Set

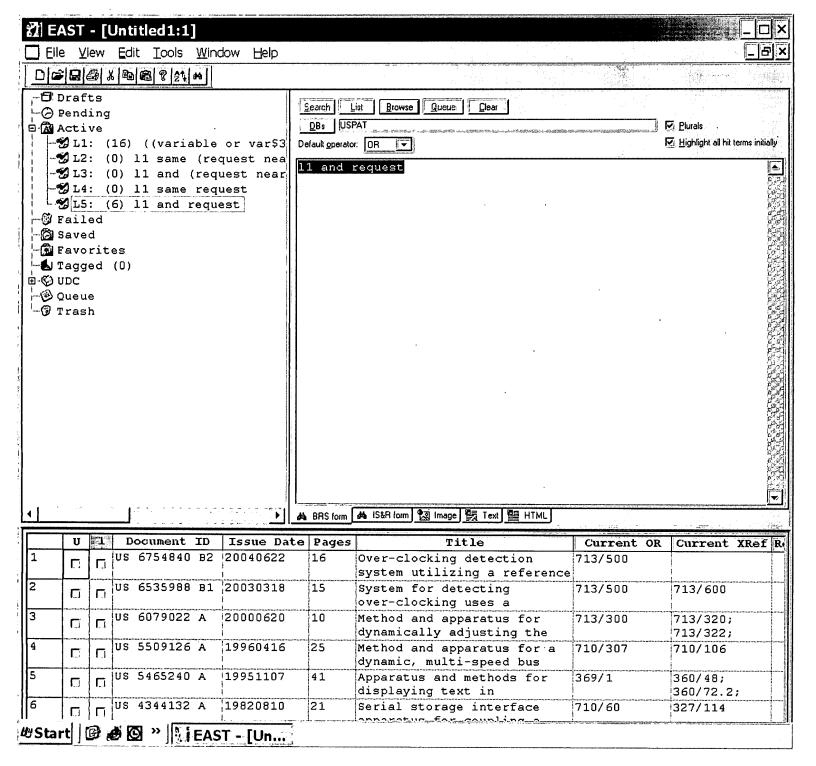
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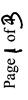
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IET CNF	IET Conference Proceeding	Walker, P.A.; Ghosh, S.;				
IEEE STD IEEE Standard	Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction: Volume 16, Issue 8, Aug. 1997 Page(s):894 - 915 Digital Object Identifier 10.1109/43.644615					
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design with hardware/software partitioning is illustrated by a number of design space exploration experiments selection with hardware/software partitioning. The communication estimation model allows for fast estimation This paper explores the problem of determining the characteristics of the communication links in a computer estimation model and show, by the use of this model, the importance of integrating communication protocol out is still sufficiently detailed as to allow the designer or design tool to efficiently explore tradeoffs between buses, CPU's, ASIC's, software code size, hardware area, and component prices. A distinct feature of the communication throughput. The integration of communication protocol selection and communication driver model is the modeling of driver processing of data (packing, splitting, compression, etc.) and its impact on throughputs, bus widths, burst/nonburst transfers, operating frequencies of system components such as system as well as determining the best functional partitioning. In particular, we present a communication carried out within the LYCOS cosynthesis system, using models of the PCI and USB protocols

Index Terms Inspec

Controlled Indexing

hardware-software codesign protocols

Non-controlled Indexing

system driver data processing estimation model functional partitioning hardware/software LYCOS cosynthesis PCI protocol USB protocol communication protocol computer



codesign Author Keywords Not Available

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